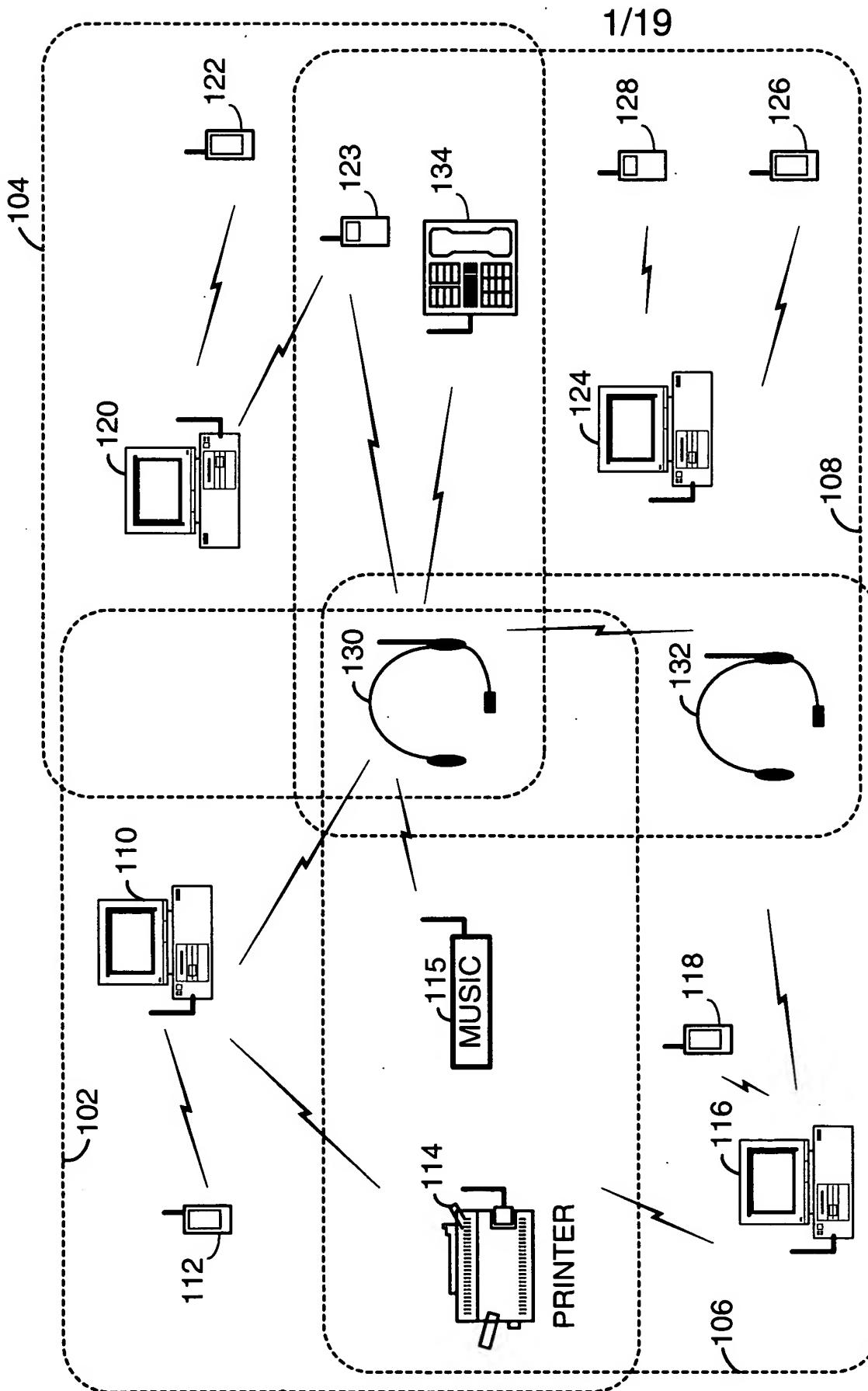


**FIG. 1**



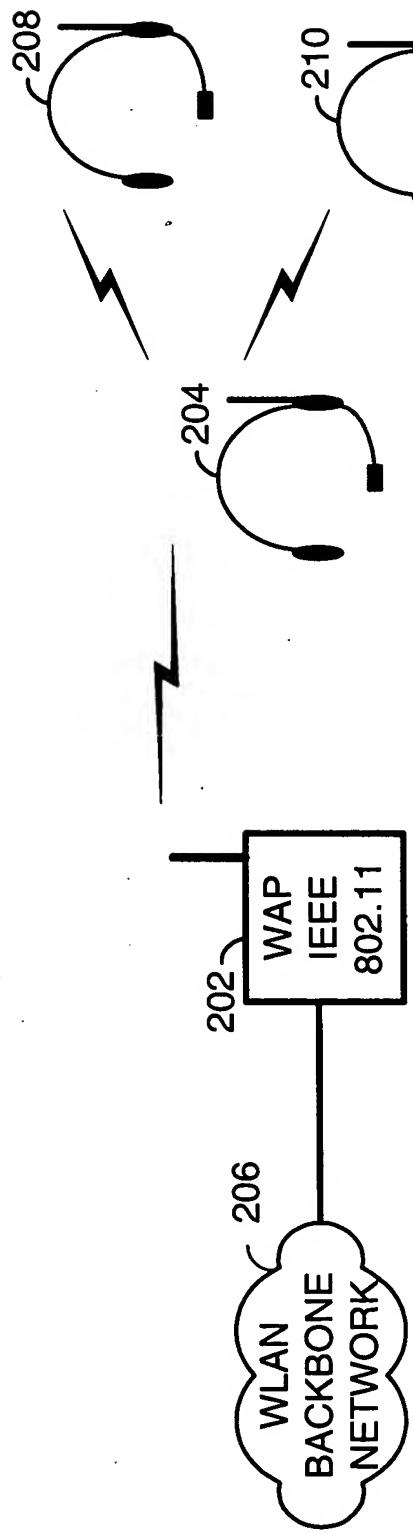


FIG. 2A

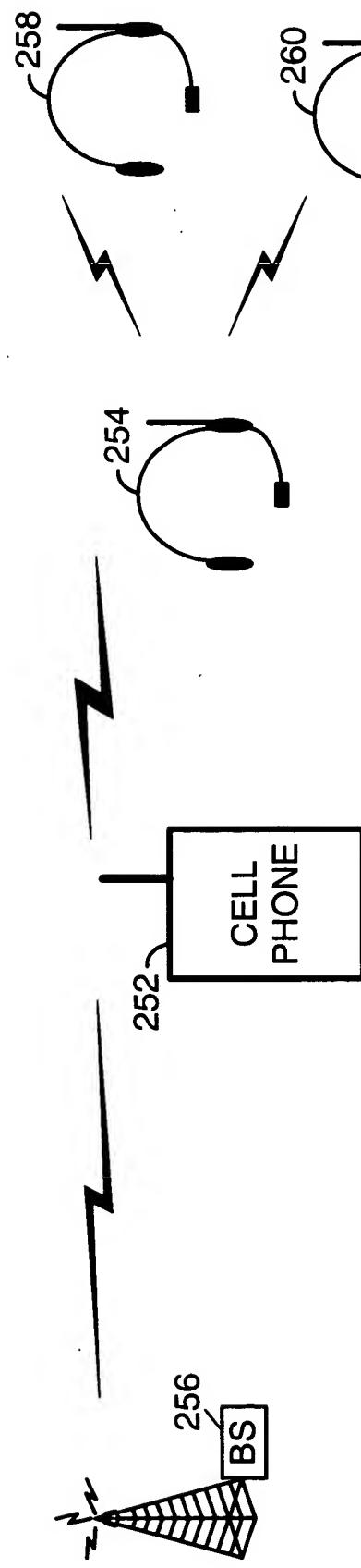


FIG. 2B

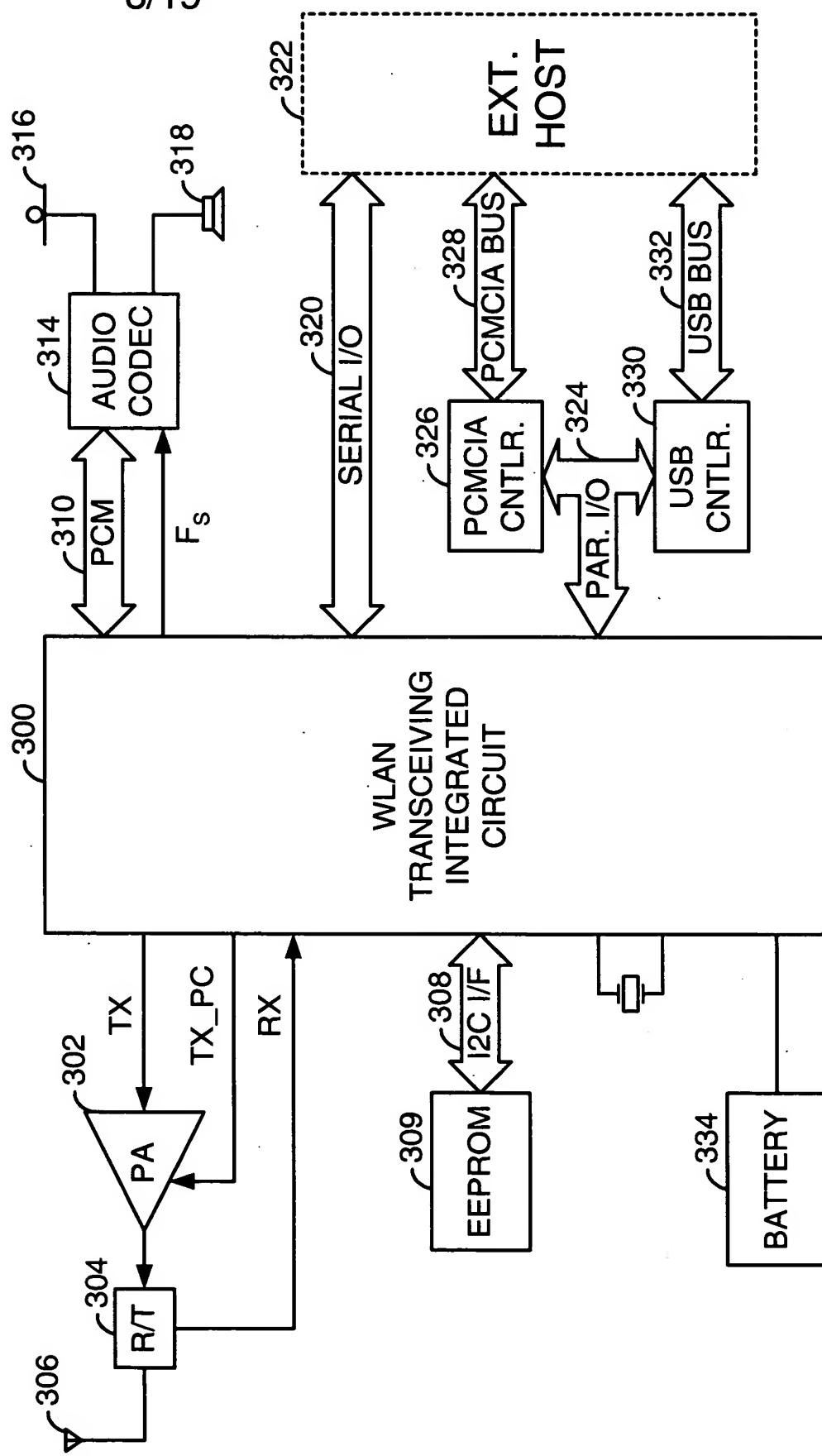
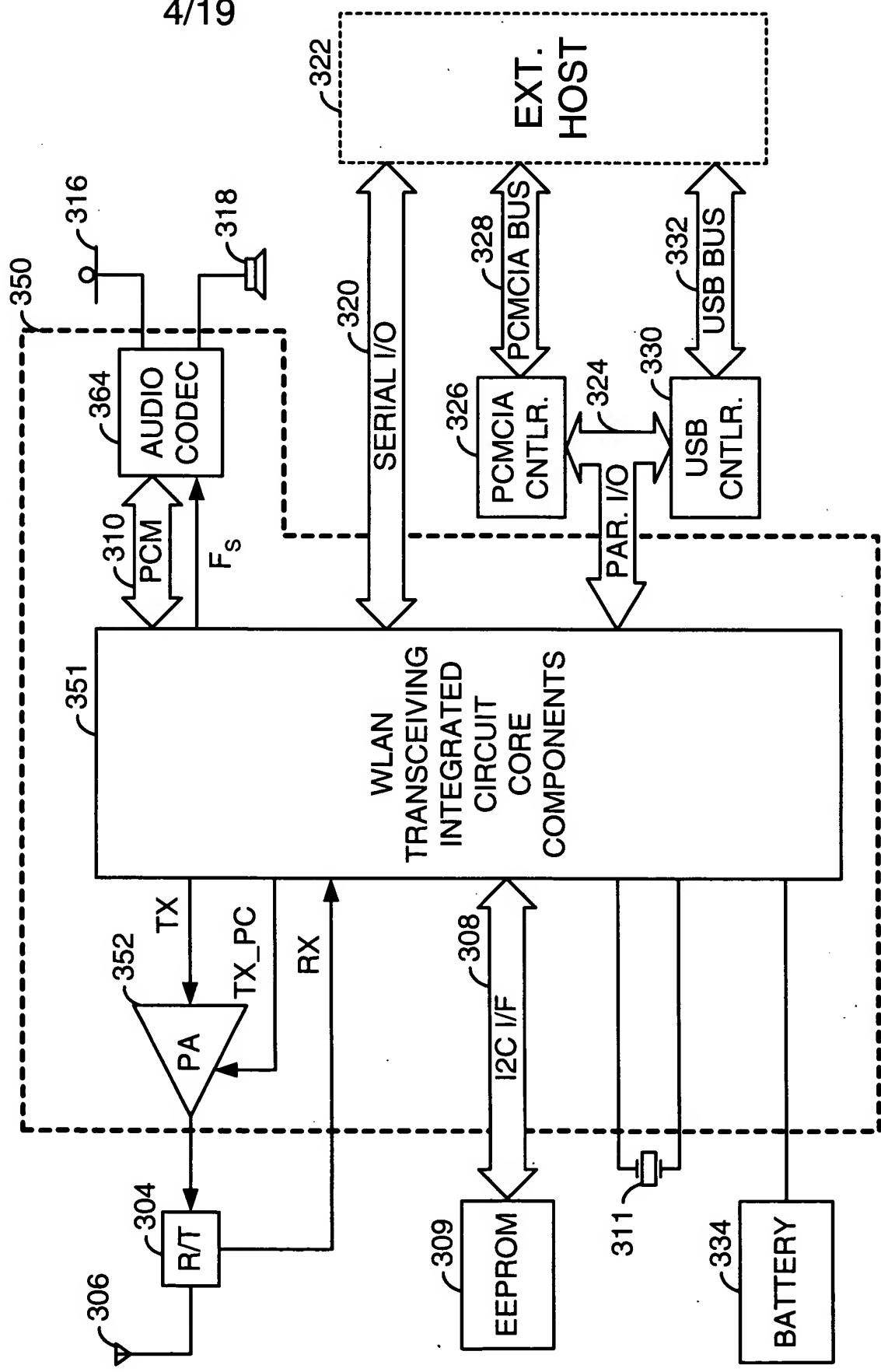
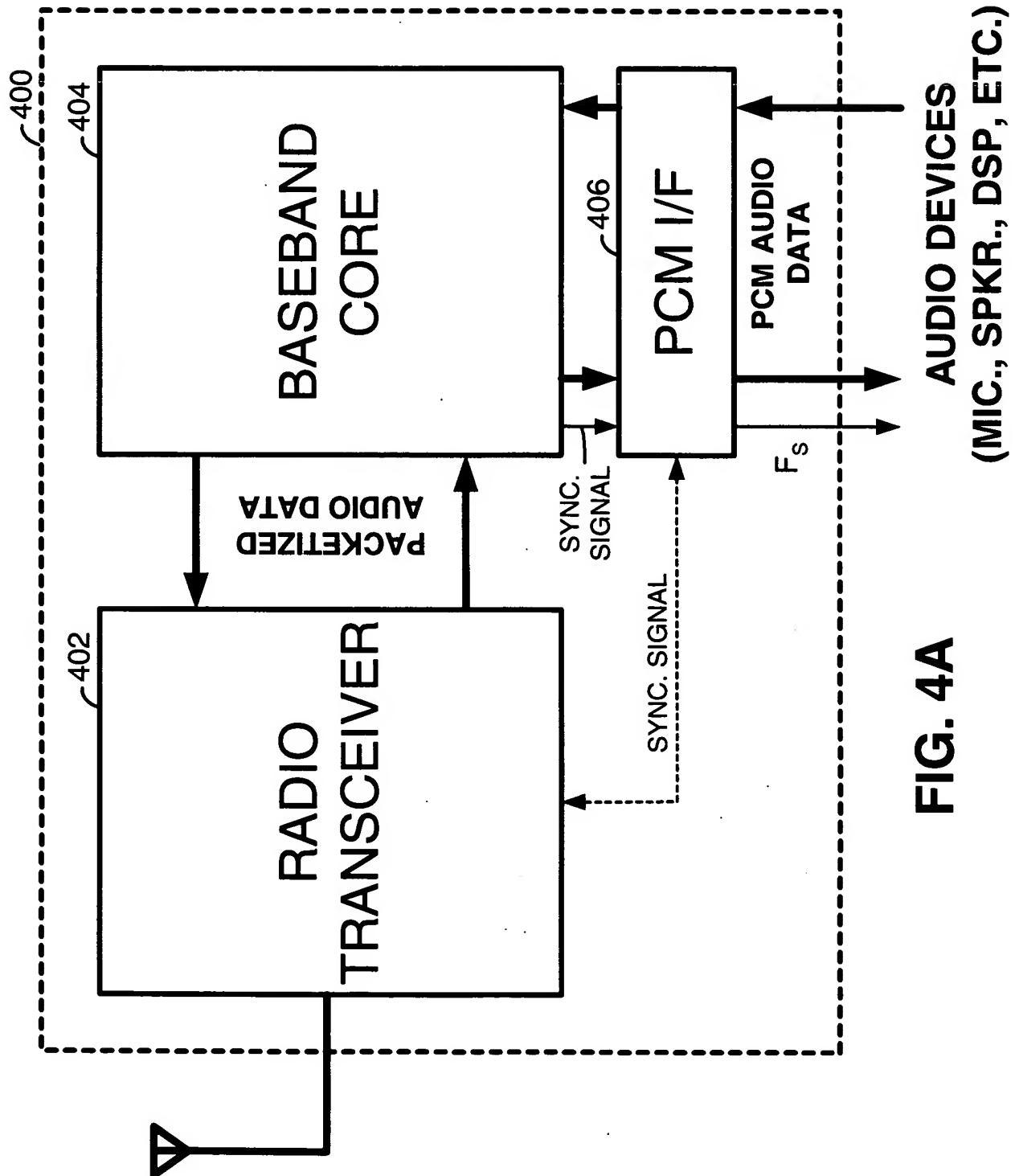
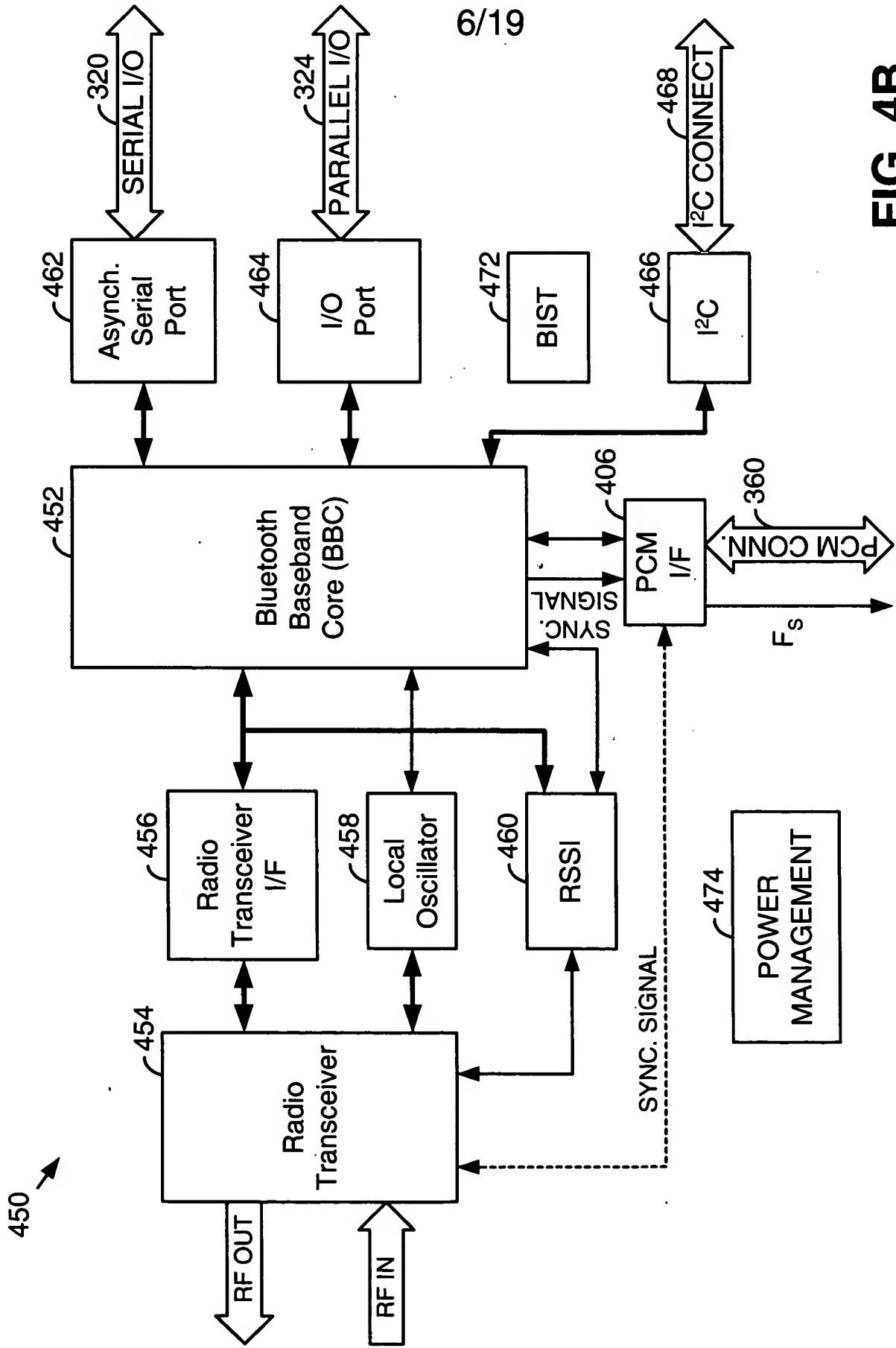


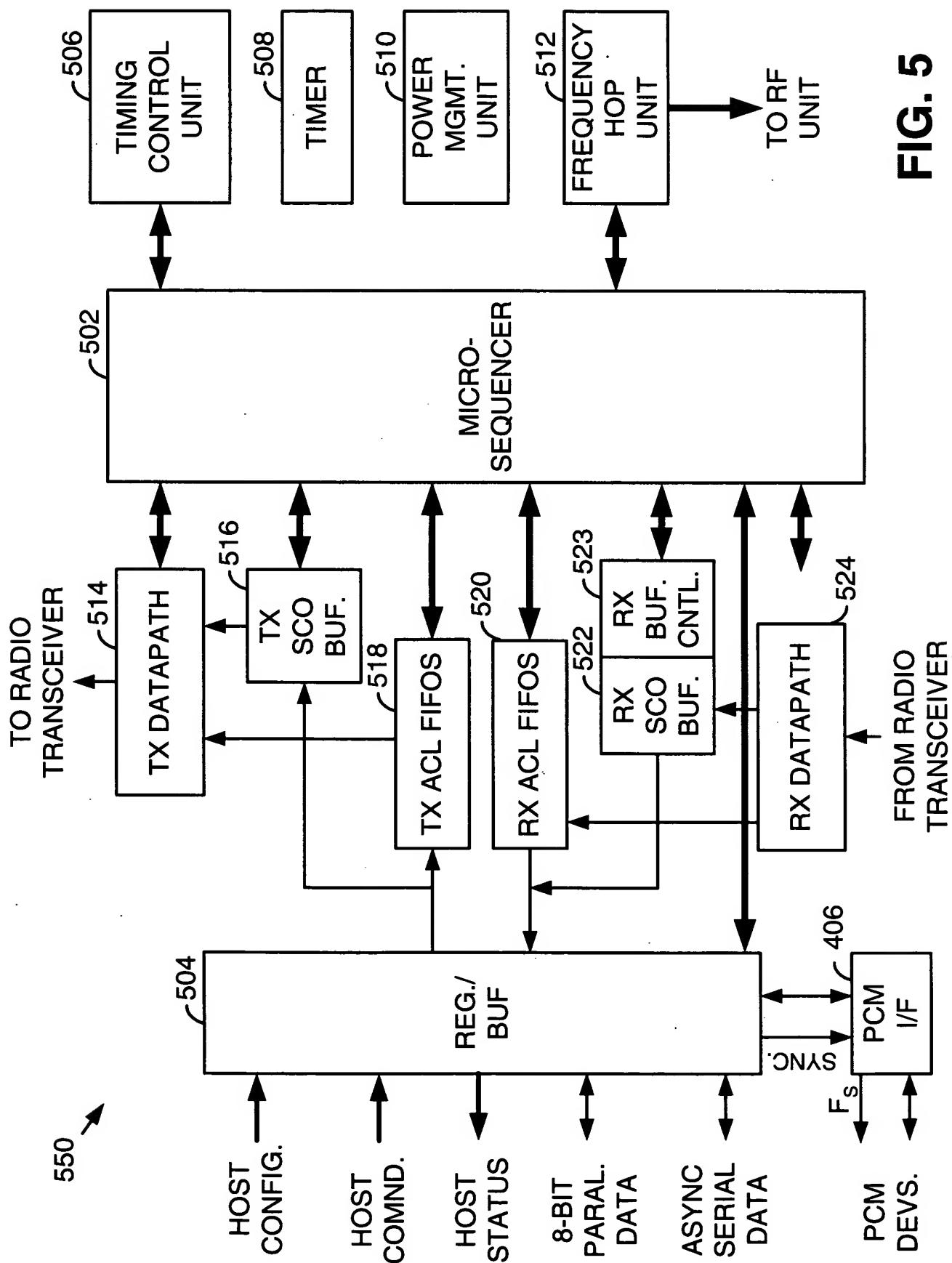
FIG. 3A

**FIG. 3B**

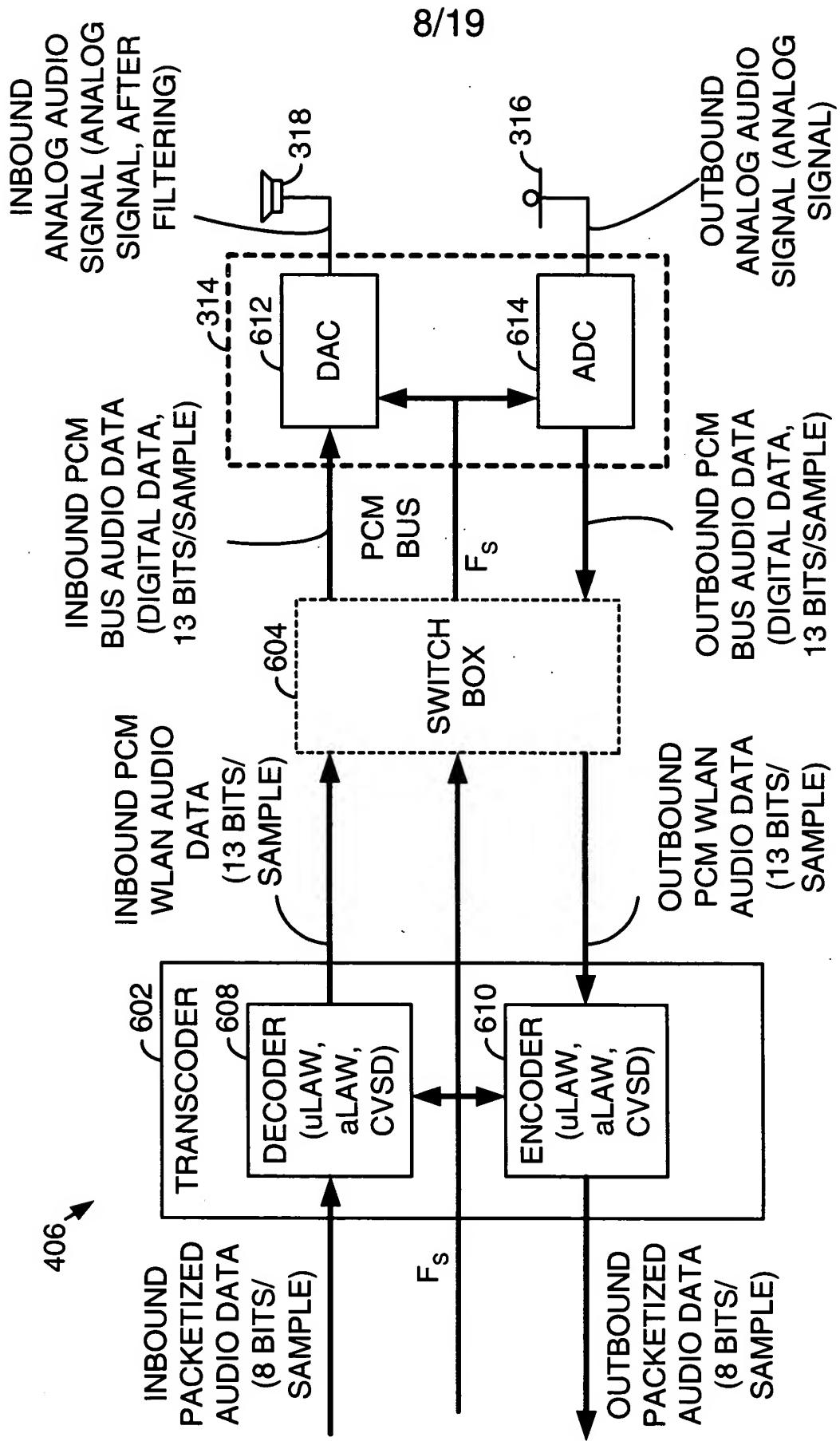


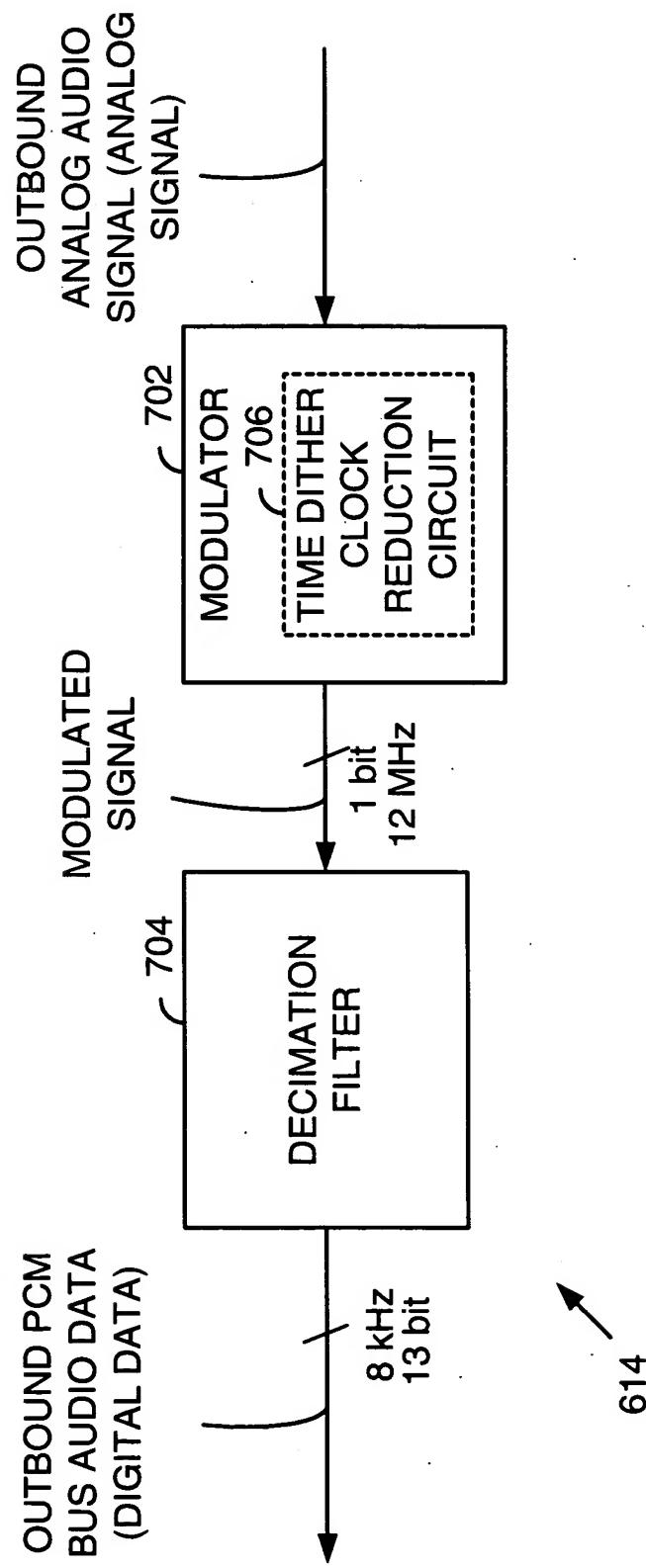
**FIG. 4B**

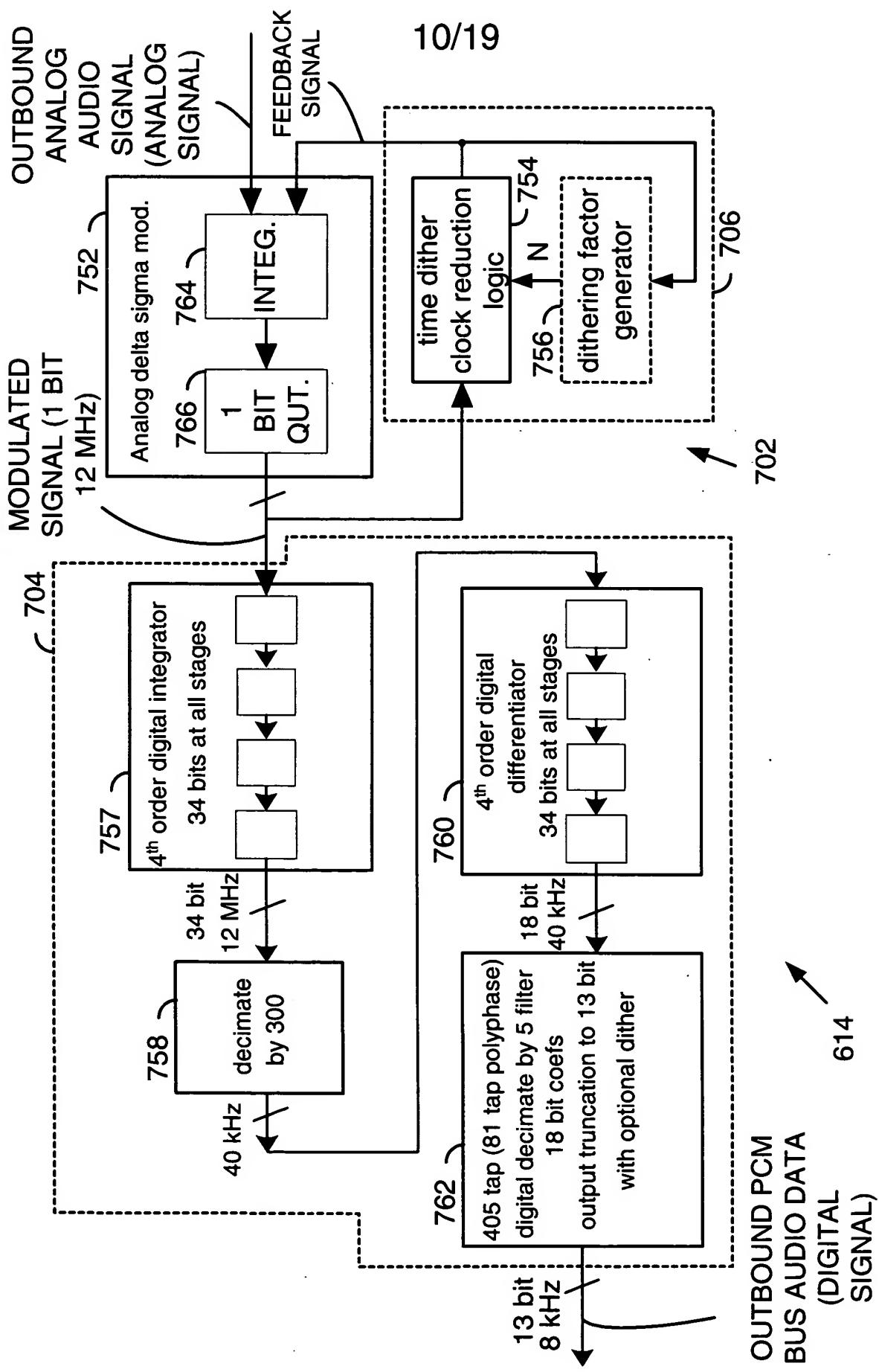


**FIG. 5**

**FIG. 6**



**FIG. 7A**



**FIG. 7B**

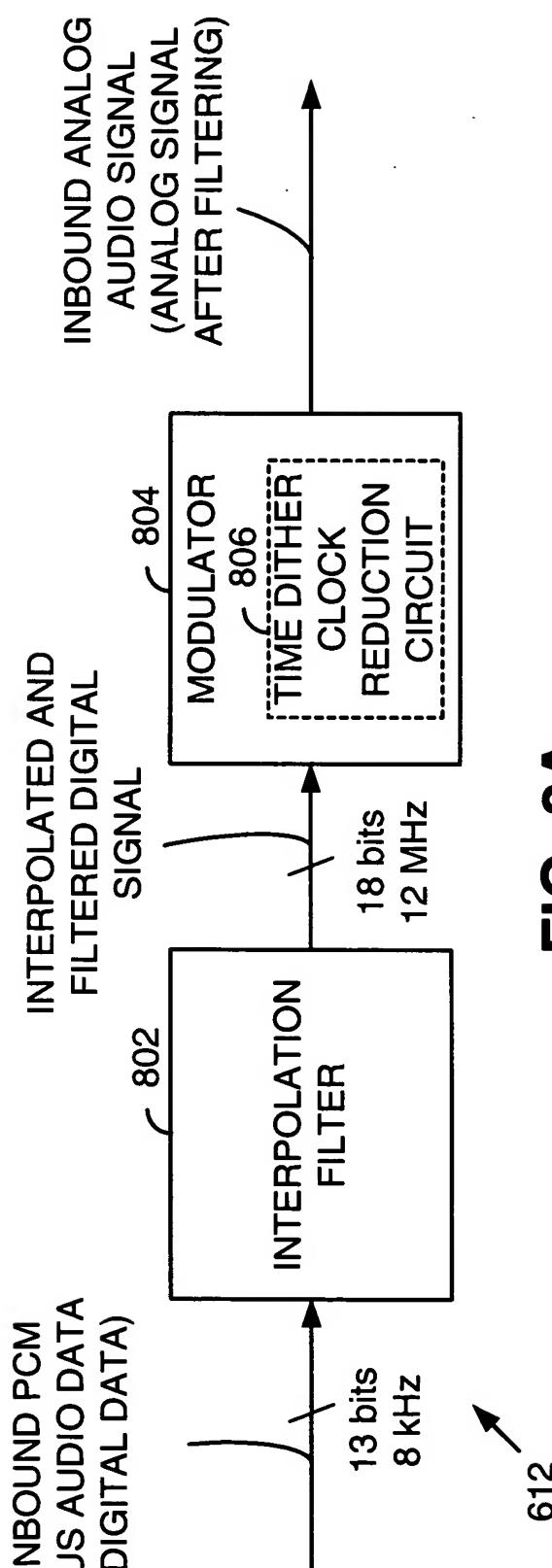


FIG. 8A

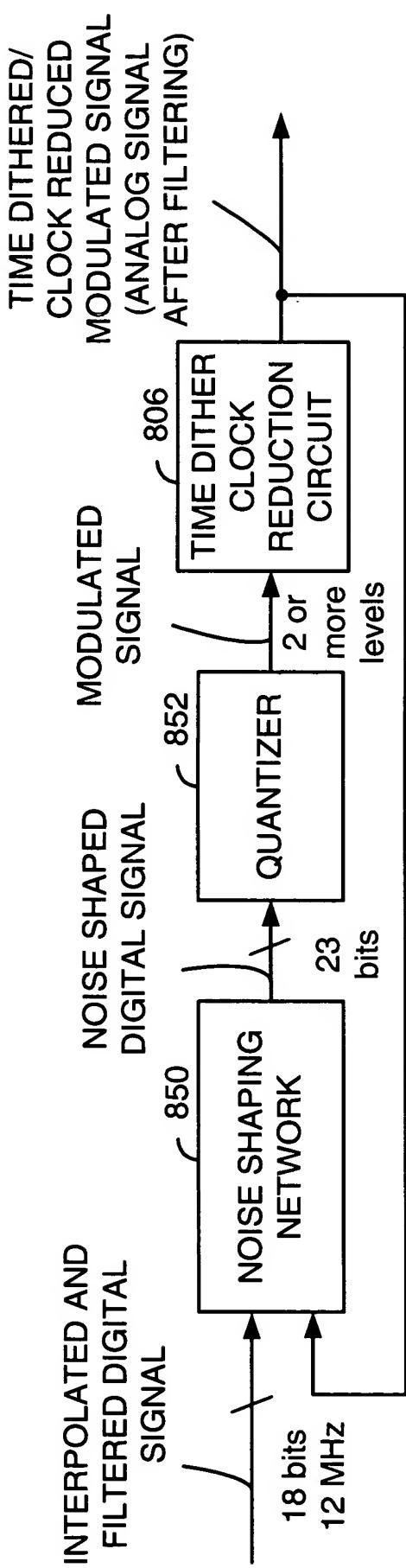
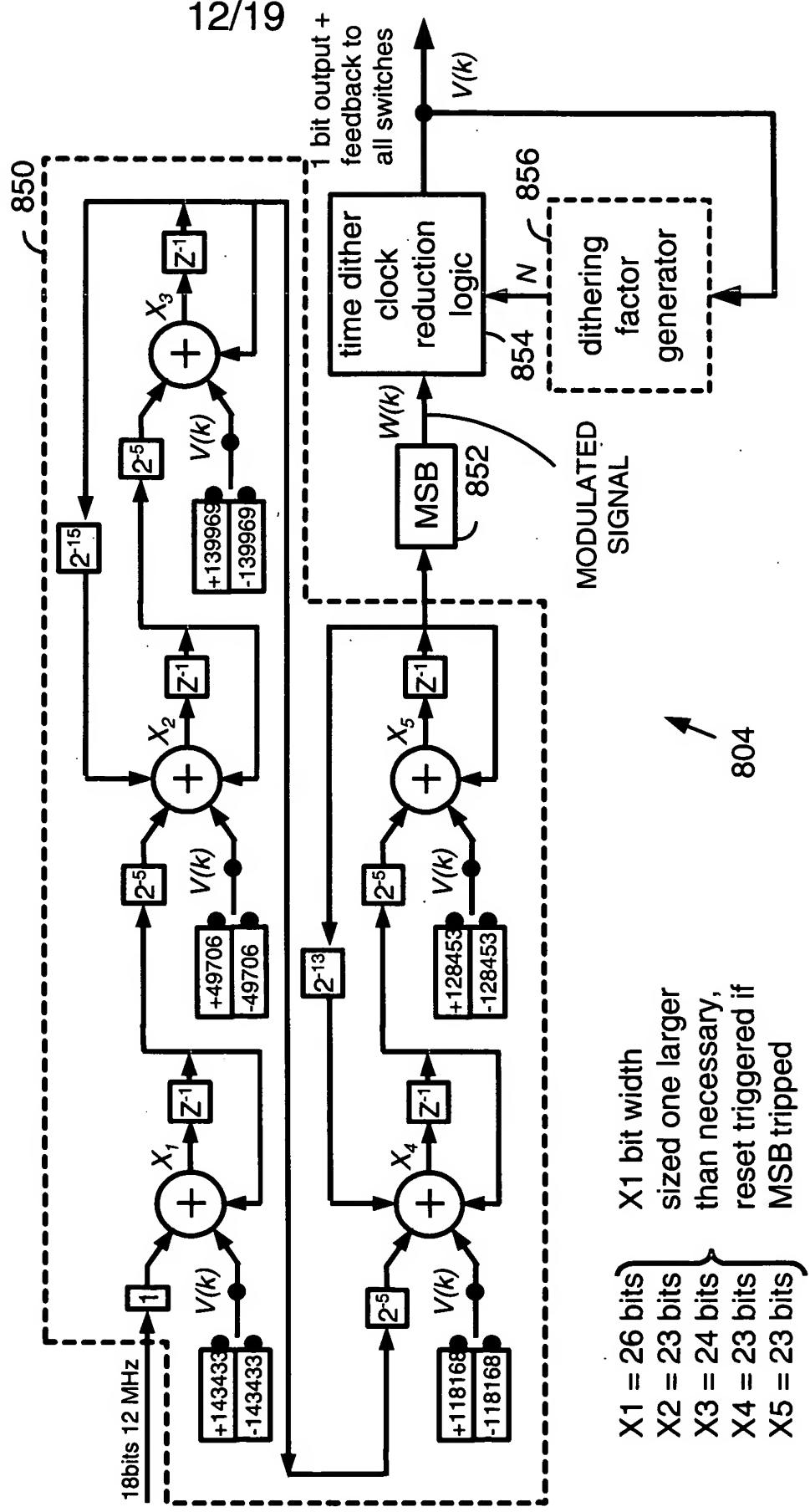


FIG. 8B

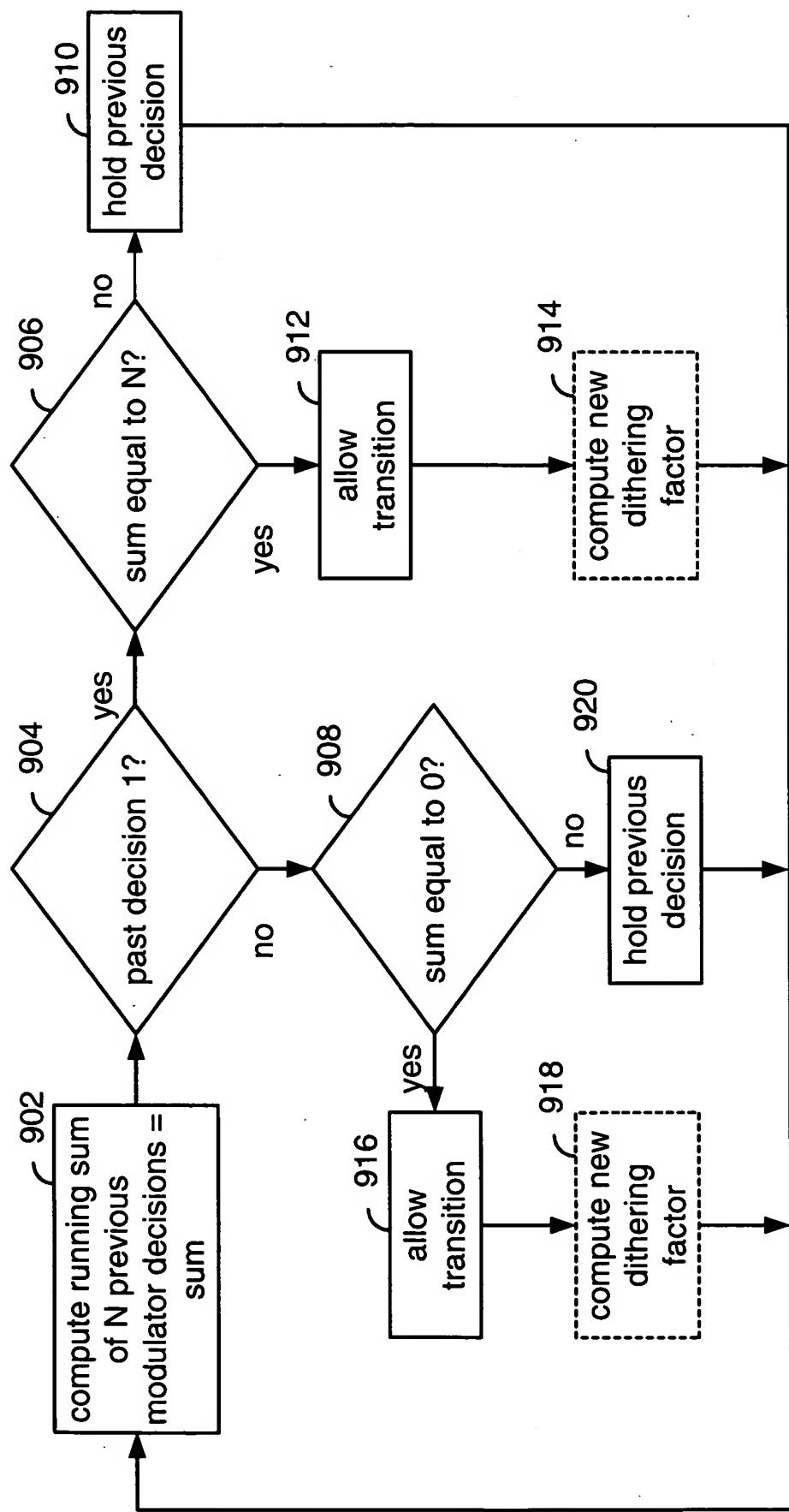


$X_1 = 26 \text{ bits}$   
 $X_2 = 23 \text{ bits}$   
 $X_3 = 24 \text{ bits}$   
 $X_4 = 23 \text{ bits}$   
 $X_5 = 23 \text{ bits}$

$X_1$  bit width  
 $X_2$  sized one larger  
than necessary,  
reset triggered if  
MSB tripped

FIG. 8C

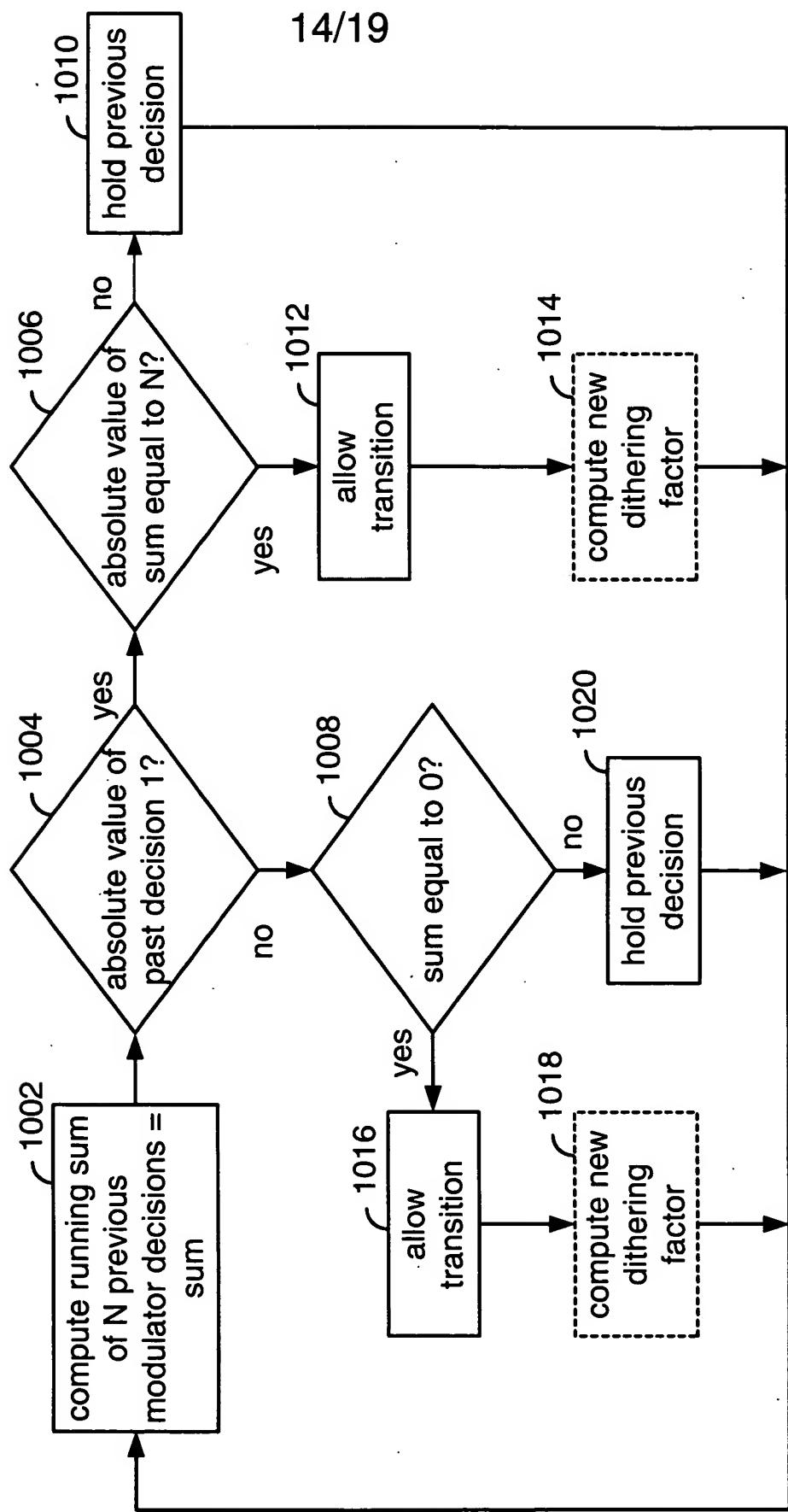
**OPERATION FOR TWO LEVEL  
QUANTIZER**



**FIG. 9**

**OPERATION FOR THREE LEVEL QUANTIZER**

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**FIG. 10**

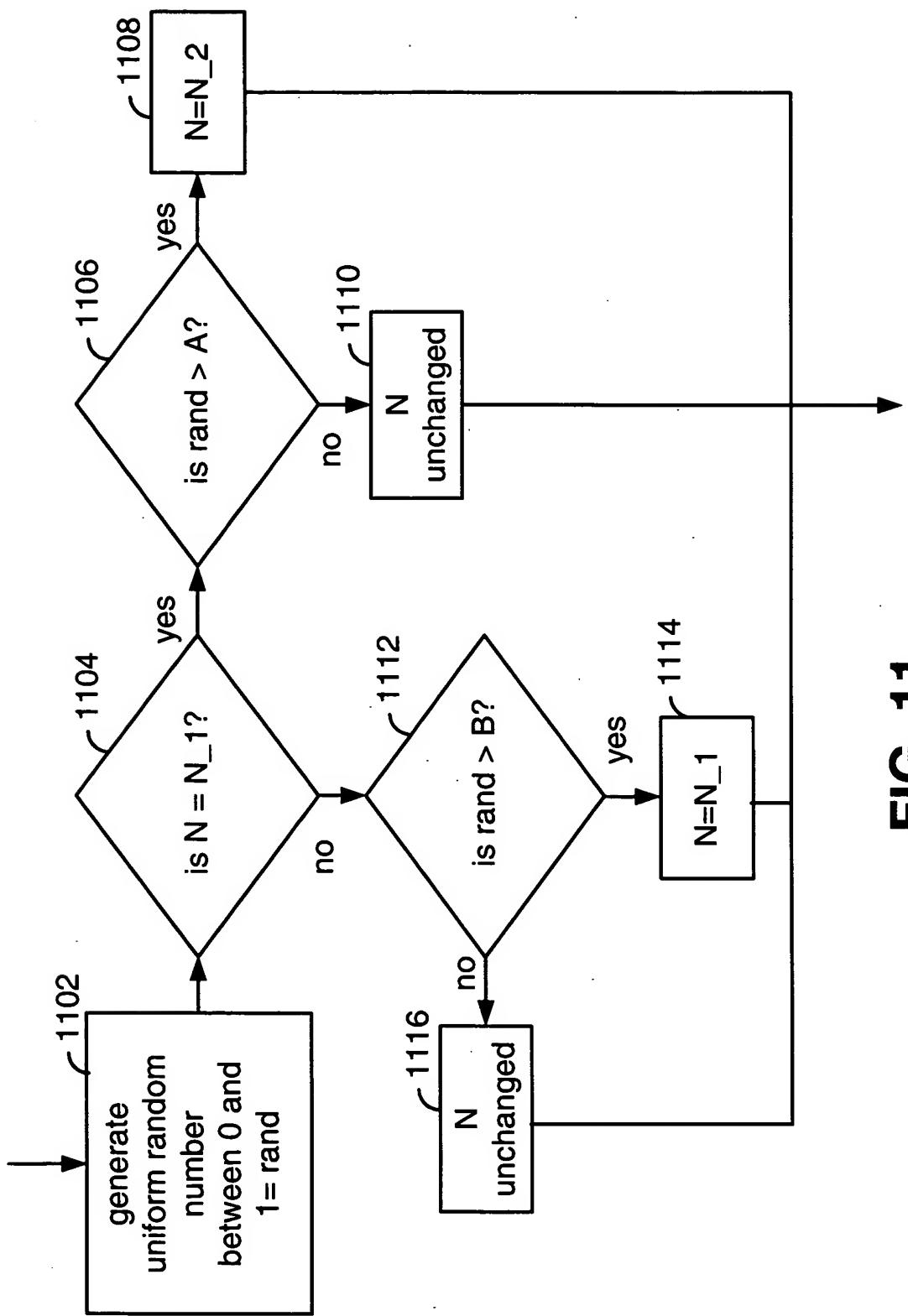


FIG. 11

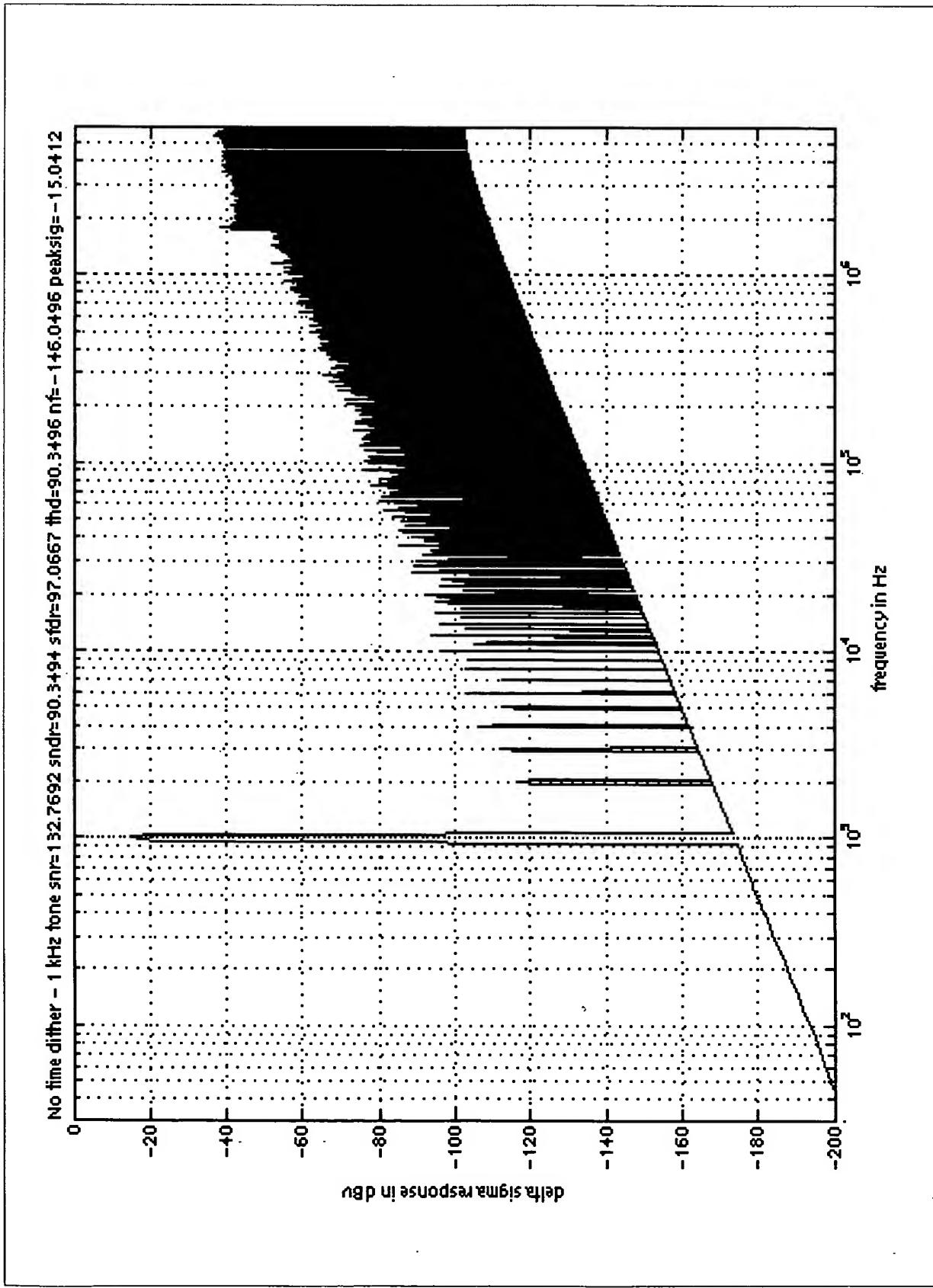


FIG. 12

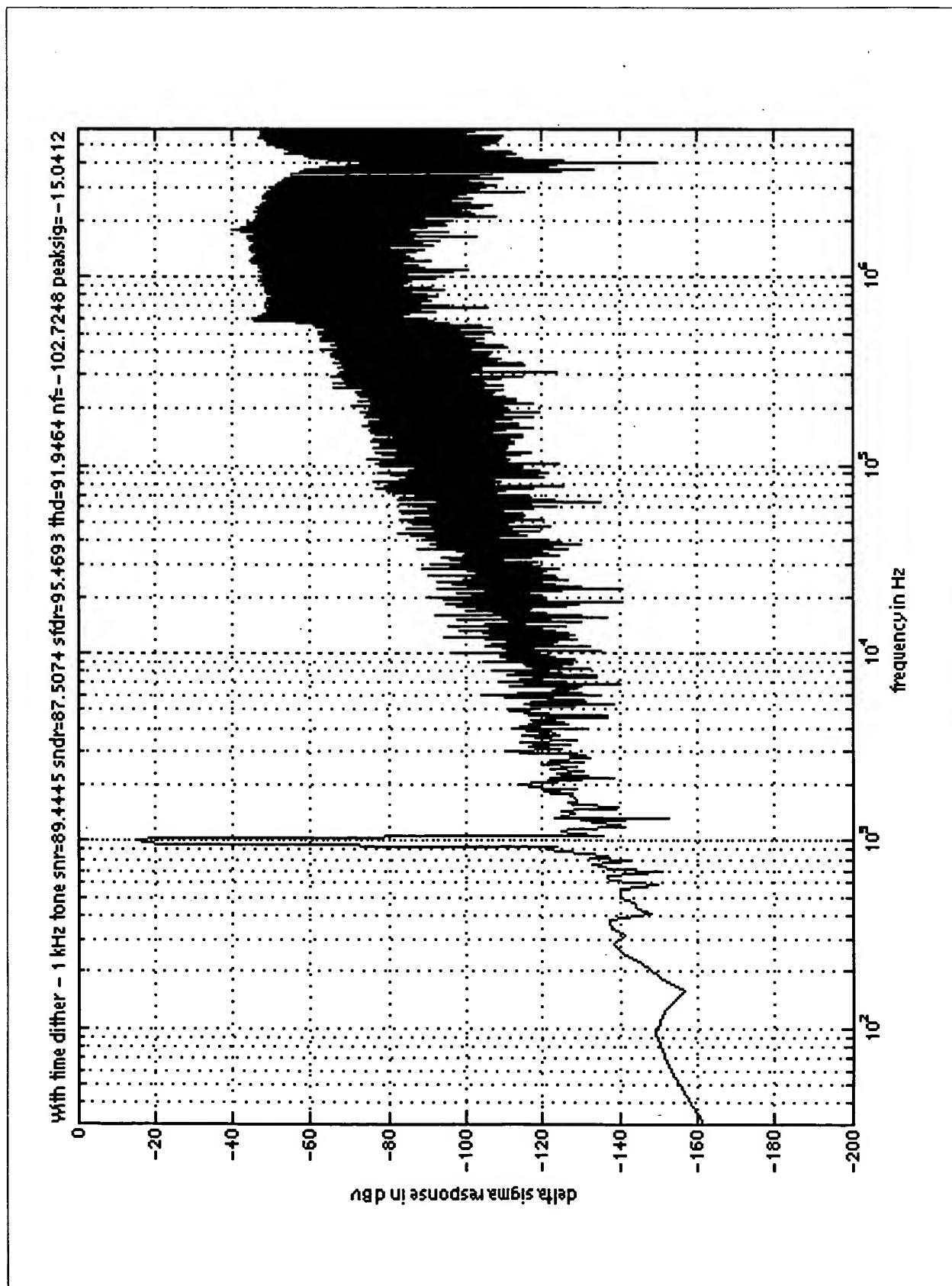
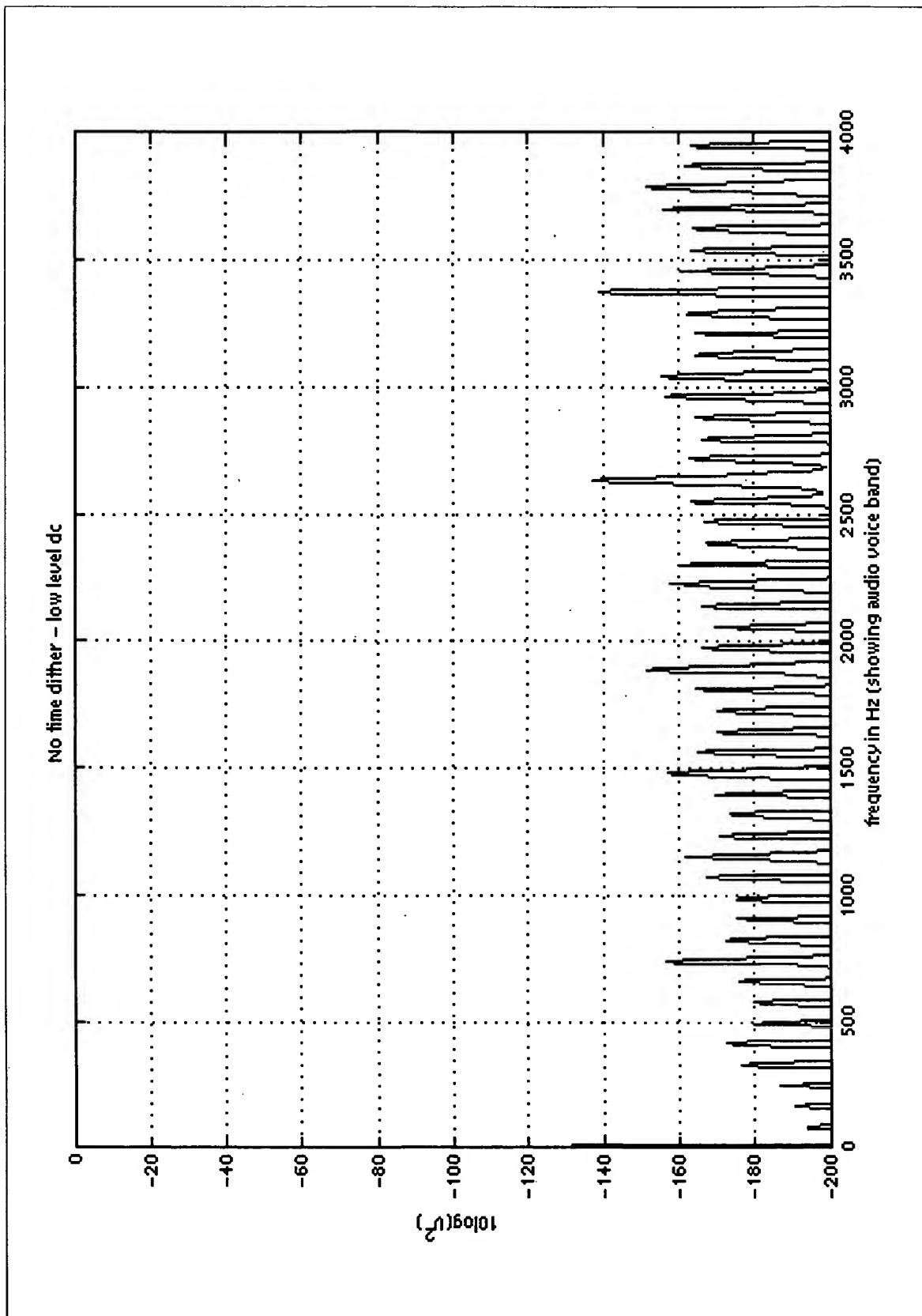
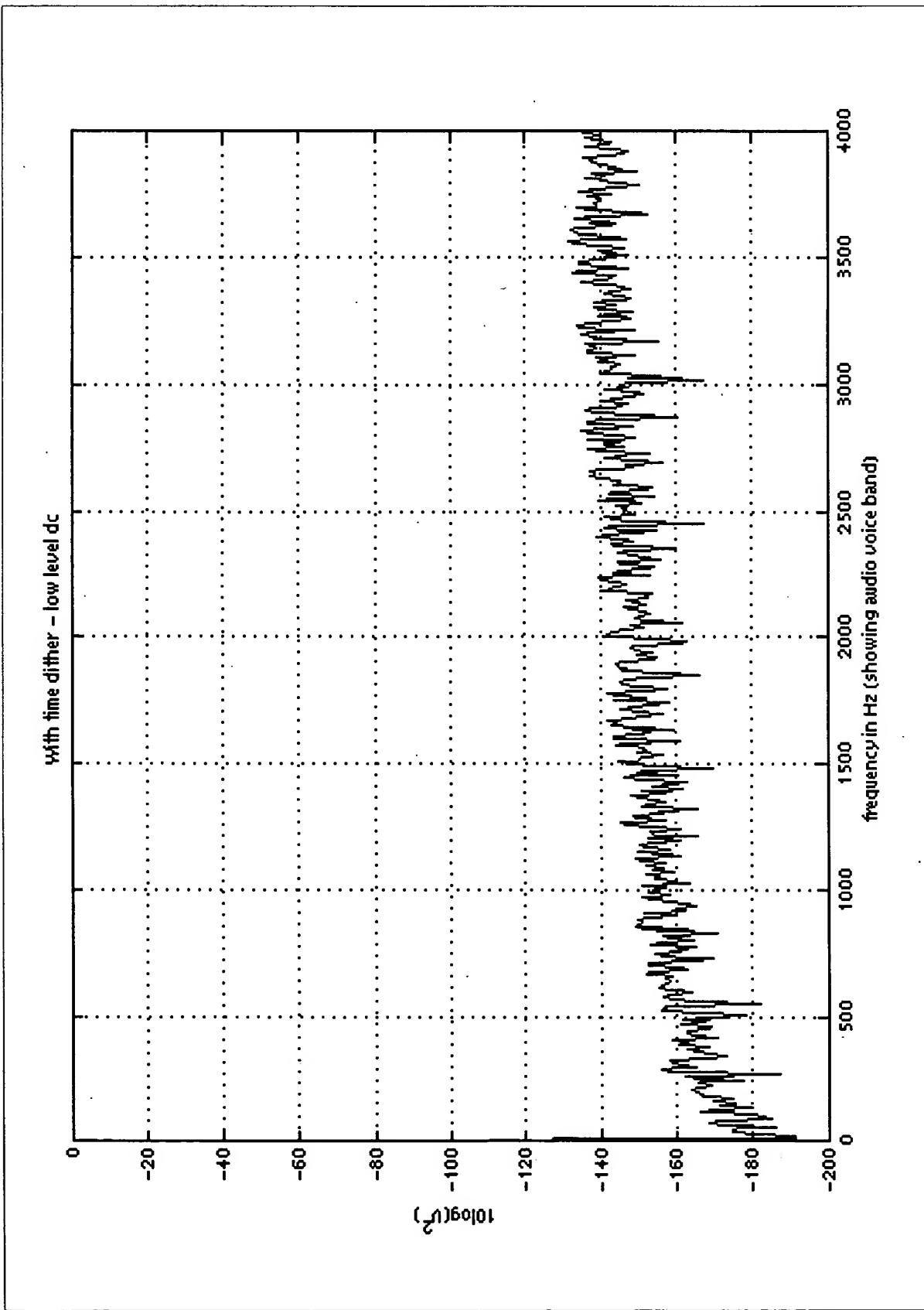


FIG. 13

**FIG. 14**

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**FIG. 15**